FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE **LIST OF DOCUMENTS CITED BY APPLICANT** *(Use several sheets if necessary)*	ATTY. DOCKET NO. TI-33162	SERIAL NO. Unknown
		APPLICANT Tod David Wolf, et al	
		FILE DATE Unknown	GROUP Unknown

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AO							
	AP							
	AQ							
	AR							

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

EA	AS	C. Bernard Shung, Paul H. Siegel, Gottfried Ungerboeck, and Hemant K. Thapar, "VLSI Architectures for Metric normalization in Viterbi Algorithm," *IEEE ICC*, pp. 1723-1728, 1990.
EA	AT	Yufei Wu and Brian D. Woerner, "Internal data width in SISO decoding module with modular renormalization," Proceedings *IEEE* Vehicular Technology Conference, Tokyo, Japan , May 2000. (5 pages)
	AU	

EXAMINER *Esaw Abraham*	DATE CONSIDERED *05/21/05*

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